## D《LLTechnologies

## Statement of Volatility - Latitude 9440 2-in-1

$\triangle$ CAUTION: A CAUTION indicates either potential damage to hardware or erasure of data and tells you how to avoid the problem.

The Latitude 9440 2-in-1 contains both volatile and non-volatile components. Volatile components lose their data immediately after power is removed from the component. Non-volatile (NV) components continue to retain their data even after power is removed from the component. The following NV components are present on the Latitude 9440 2-in-1 system board.

Table 1. List of Non-Volatile Components on System Board

| Description | Reference Designator | Volatility Description | User Accessible for external data | Remedial Action (action necessary to erase data) |
| :---: | :---: | :---: | :---: | :---: |
| Embedded Flash in embedded controller MEC5200 | UE1 | 384 KB Code/Data SRAM <br> 320 KB code/64 KB Data optimized for performance | No | N/A |
| Panel EEDID EEPROM | Part of panel assembly | Non-Volatile memory, FHD+ 256 bytes, QHD+ 256 bytes. | No | Part of panel assembly |
| System BIOS | UH8 | Non-Volatile memory, <br> 64 MB *1 System BIOS and Video BIOS for basic boot operation, PSA (on board diags), PXE diags. | No | N/A |
| System Memory LPDDR5 LPDDR5x memory | On board DRAM+ UD1/UD2/ UD3/UD4 | Volatile memory in OFF state System memory size will depend on LPDDR5 /5x , $32 \mathrm{~Gb} / 64 \mathrm{~Gb}, 128 \mathrm{~Gb}(\times 32)$ per package | No | N/A |
| System memory SPD EEPROM | Part of memory module assembly | Non-Volatile memory, Stores memory manufacturer data and timing information into BIOS code. | No | N/A |
| Hard drive(s) | User replaceable one | 2230 M. 2 type SSD (PCIE interface) | Yes | Low level format |
| Card Reader | uSD 4.0 Card reader controller F/W UR1 | PCIE interface of embedded Flash memory | No | N/A |
| Touch screen Embedded Flash | Part of panel assembly | I2C interface of embedded Flash memory | No | Part of panel assembly |


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| :---: | :---: | :---: | :---: | :---: |
| Accelerometer (secondary) LSM6DSOUSTR | $\begin{aligned} & \text { UAC1 } \\ & \text { for 360 2-in-1 } \end{aligned}$ | I2C interface of embedded Flash memory | No | N/A |
| Accelerometer + Gyro LIS2DW12TR | $\begin{aligned} & \hline \text { U733 } \\ & \text { For } 360 \text { 2-in-1 } \end{aligned}$ | I2C interface of embedded Flash memory | No | N/A |
| Compass LIS2MDLTR | U734 | I2C interface of embedded Flash memory | No | N/A |
| $\begin{aligned} & \text { ALS } \\ & \text { TCS3430 } \end{aligned}$ | Part of camera assembly | I2C interface of embedded Flash memory | No | Part of camera assembly |
| $\begin{aligned} & \text { TPM } 2.0 \\ & \text { ST33HTPH2×32AHD8 } \\ & \text { ST33HTPH2X32AHE4 } \\ & \text { NPCT750JADYX } \\ & \text { NPCT760JABYX } \end{aligned}$ | U712 | SPI interface of embedded Flash memory | No | N/A |
| MCU <br> ATSAMD21E16BMUTN02 | U717 | USB interface of embedded Flash memory | No | N/A |
| CVF CLOVER_FALLS | UCVF1 | eSPI interface of embedded Flash memory | No | N/A |
| RF proximity sensor SX9331IULTRT | U16 | SMBUS interface of embedded Flash memory | No | N/A |
| PD PTTPS65994BF | UPD1/UPD2/UPD3 | I2C interface of embedded Flash memory | No | N/A |
| Hayden Bridge <br> TBT Re-Timer | URT1/URT2/URT3 | I2C interface of embedded Flash memory | No | N/A |
| CV3 ROM | U1 | Non-Volatile memory. 128 Mbit (16 MB) for CV3 F/W flash. | No | N/A |
| Fingerprint Sensor | Module | USB interface of embedded Flash memory | No | N/A |
| Touchpad | Module | I2C interface of embedded Flash memory | No | N/A |
| Camera Sensor | Module | I2C interface of embedded Flash memory | No | N/A |

CAUTION: All other components on the system board erase data if power is removed from the system. Primary power loss
$\triangle$ (unplugging the power cord and removing the battery) destroys all user data on the memory (DDR3, 1067 MHz ). Secondary power loss (removing the on-board coin-cell battery) destroys system data on the system configuration and time-of-day information.
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